

Notice of References Cited	Application/Control No. 10/815,478		Applicant(s)/Patent Under Reexamination BALL, JAMES LORAN	
	Examiner George D. Zalepa		Art Unit 2183	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,219,833	04-2001	Solomon et al.	717/149
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Intel, Inc., "IA-32(R) Architecture Software Developer's Manual", 2002, Volumes 1-2
	V	Intel, Inc., "P6 Family of Processors Hardware Developer's Manual", September 1998
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	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.